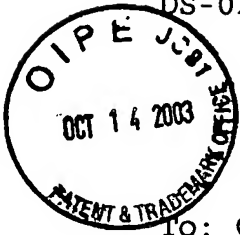


DS-02-020



October 6, 2003

To: Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572
28 Davis Avenue
Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/614,663 07/07/03

Rainer Krenzke et al.

COMPARATOR WITH HIGH-VOLTAGE INPUTS
IN AN EXTENDED CMOS PROCESS FOR
HIGH VOLTAGE LEVELS

Grp. Art Unit: _____

INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation
In An Application.

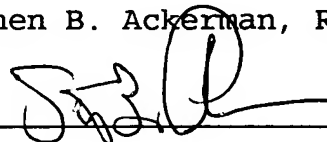
The following Patents and/or Publications are submitted to
comply with the duty of disclosure under CFR 1.97-1.99 and
37 CFR 1.56. Copies of each document is included herewith.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first class
mail in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450, on October 10, 2003.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

 10/10/03

DS-02-020


U.S. Patent 6,377,075 to Wong, "High Voltage Protection Circuit on Standard CMOS Process," describes a high voltage protection circuit on standard CMOS process.

U.S. Patent 6,424,183 to Lin et al., "Current Comparator," discloses a current comparator realized in a low voltage CMOS process.

U.S. Patent 6,452,440 to Rapp, "Voltage Divider Circuit," discloses a voltage divider circuit, wherein a charge pump system includes a charge pumping circuit for outputting a high voltage V_{pp} at a node.

DS-02-021 to Rainer Krenzke et al., Serial No. 10/615,123 Filed 07/08/03, "Slew Rate Sensing and Control of a High-Voltage Output Driver for a Variable Voltage Range and Variable Output Load," discusses slew rate controlled driver circuits within regulator loops realized with integrated-circuit technologies.

Sincerely,

A handwritten signature in black ink, appearing to be 'SBA', written over the printed name.

Stephen B. Ackerman,
Reg. No. 37761

Hydrogen Membrane

10/614,663

Rainer Krenzke et al.

07/07/03

Group Art Unit

(Use several sheets if necessary)

U. S. PATENT DOCUMENTS

PLANO DATE
X APPROPRIATE

11/4/99

6/26/01

110101

FOREIGN PATENT DOCUMENTS

Translation

YES	NO
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DS-02-021 to Rainer Krenzke et al., Serial #
10/615, 123, Filed 07/08/03, "Slew Rate Sensing
and Control of a High-Voltage Output Driver for
a Variable Voltage Range and Variable Output Load".

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant